

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of

Examiner: Robert Kunemund

Belouet

Group Art Unit: 1792

Serial No.: 10/594,652

Filed: September 27, 2006

For: METHOD OF FABRICATING POLYCRYSTALLINE SILICON PLATES

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**AMENDMENT**

**SUBMITTED VIA EFS-WEB**

**Mail Stop AF**

Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

SIR:

In connection with the above-referenced application, please enter the following remarks in response to the Final Office Action dated April 14, 2010. An RCE is attached.

**Amendments to the Claims** are reflected on the listing of claims beginning on page 2 of this paper.

**Remarks** begin on page 10 of this paper.